

EAST Search History

@ 10/767,046

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(scan chain AND master-slave configuration AND shift register latches AND programming instruction AND shift clock pulse AND scan clock pulse AND operating region AND failing region).clm.	US-PGPUB; USPAT	ADJ	ON	2006/06/14 17:19
L2	3630	scan chain\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:20
L3	613309	integrated circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:20
L4	295	scan test pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:20
L5	118	I2 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:21
L6	107	I5 and I3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:21
L7	52	failing region	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:21

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L8	9496	operating region	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:21
L9	1	I6 and I7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:21
L10	1	I6 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:24
L11	162	scan clock pulse	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:22
L12	837	shift clock pulse	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:22
L13	15230	master latch or first latch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:23
L14	16479	slave latch or second latch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:23
L15	37	I12 and I14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:23

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L16	25	I11 and I13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:23
L17	1	I6 and (I15 or I16)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:24
L18	6	I6 and (I13 or I14)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:52
L19	1	I18 and (I11 or I12)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:26
L20	629	detect\$3 and (multiple defect\$1 same level sensitive scan design or (LSSD))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:27
L21	3	I18 and I20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:28
L22	1	I21 and (I7 or I8)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:28
L23	27814	714/726 or 714/733 or 714/734 or 714/30 or 714/738 or 714/724 or 714/37 or 714/39 or 714/718 or 714/? or 365/200 or 365/201 or 716/4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:58

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L24	4	I18 and I23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/06/14 17:58
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